FAST - [Default EAST Workspace 1600x1200.wsp:1]

File View Edit Tools Window Help

- Drafts
  - BRS: plurality
  - BRS: plurality near10 (shift$3)
- Pending
- Active
  - L1: (277694) packed
  - L2: (3806) 1 near10 (addition or add or adding)
  - L3: (1148) 1 near10 (shift$3)
  - L4: (85) 2 same 3
  - L5: (311860) instruction
  - L6: (60) 4 same 5
  - L7: (73) 2 with 3
  - L8: (48) 5 with 7
  - L9: (12) 6 not 8
  - L10: (25) 4 not 6
  - L11: (3262) simd
  - L12: (69079) (shift$3) with (addition or add or adding)
  - L13: (71) 11 same 12
  - L14: (68) 13 not 4
  - L19: (84124) plural$3 near10 (addition or add or adding)
  - L20: (49251) plural$3 near10 (shift$3)
  - L21: (921) 19 same 20
  - L22: (285084) single instruction or packed or simd
  - L23: (2) 21 same 22
  - L26: (25) 21 and 22
- Failed
- Saved
- Favorites

Search ... Browse ... Clear

DBs US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB   Plurals

Default operator ADJ   Highlight all hit terms initially

21 and 22

BRS form IS&R form Image Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrie	Inventor	S	C	P	3	
1			US 20040202965 A1	20041014	25	Phase conflict resolution for pho	430/311	257/9;		Pierrat, Christophe et al.					
2			US 20040191650 A1	20040930	44	Phase shift masking for comple	430/5	716/19		Pierrat, Christophe					
3			US 20040090907 A1	20040513	40	Fast linear feedback shift regist	370/208			An, Wei					
4			US 20040058484 A1	20040325	19	Crystallization apparatus, crysta	438/166	257/E21.413;		Taniguchi, Yukio et al.					
5			US 20020197543 A1	20021226	27	Phase conflict resolution for pho	430/5	257/6;		Pierrat, Christophe et al.					
6			US 20020127479 A1	20020912	46	Phase shift masking for comple	430/5			Pierrat, Christophe					
7			US 20020002671 A1	20020103	11	Parallel processing of multiple d	712/300			Symes, Dominic Hugo					
8			US 6733929 B2	20040511	46	Phase shift masking for comple	430/5	430/30;		Pierrat; Christophe					

Hits Details HTML

Ready NUM